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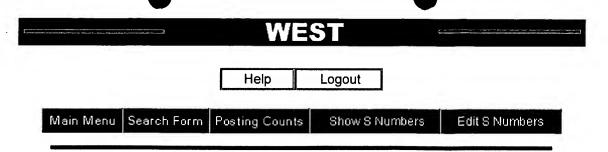
**Date:** 12/14/1999

**Time:** 09:03

	Total	USPAT	USOCR	EPO	JPO	Derwent
1	133					
2	133					

	Search Terms
95/500.17"	
95/500.17".CCLS.	

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#### Search Results -

Term	Documents
ASIC.USPT.	6147
ASICS.USPT.	2122
(9 AND ASIC).USPT.	143

Database: US	Patents Full-Text Database	•	
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	19 and asic	<b>_</b>	
Refine Search	: <u> </u>		
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#### Search History

DB Name	<u>Query</u>	Hit Count	Set Name
USPT	19 and asic	143	<u>L10</u>
USPT	(multiplexor or mux) and fpga	328	<u>L9</u>
USPT	multiplexor and fpga	61	<u>L8</u>
USPT	16 and multiplexor	13	<u>L7</u>
USPT	fpga and asic	583	<u>L6</u>
USPT	fpga and aic	9	<u>L5</u>
USPT	fpga and asla	1	<u>L4</u>
USPT	12 and interconnect and routing	141	<u>L3</u>
USPT	11 and multiplex\$	284	<u>L2</u>
USPT	fpga and application specific and integrated circuit	476	<u>L1</u>



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Main Menu	Search Form	Posting Counts	Show S Numbers	Edit 8 Numbers

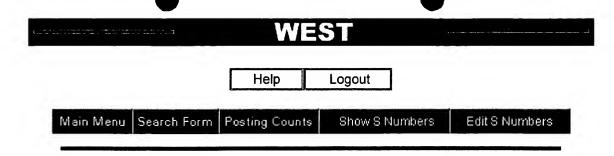
# Search Results -

Term	Documents
TRIMBERGER.USPT.	179
TRIMBERGERS	0
TRIMBERGER.INUSPT.	62

Database:	JS Patents Full-Text Database	₹
Refine Sea	trimberger.in.	

# **Search History**

DB Name	<u>Query</u>	Hit Count	Set Name
USPT	trimberger.in.	62	<u>L12</u>
USPT	5083083.pn.	1	<u>L11</u>
USPT	19 and asic	143	<u>L10</u>
USPT	(multiplexor or mux) and fpga	328	<u>L9</u>
USPT	multiplexor and fpga	61	<u>L8</u>
USPT	l6 and multiplexor	13	<u>L7</u>
USPT	fpga and asic	583	<u>L6</u>
USPT	fpga and aic	9	<u>L5</u>
USPT	fpga and asla	1	<u>L4</u>
USPT	12 and interconnect and routing	141	<u>L3</u>
USPT	11 and multiplex\$	284	<u>L2</u>
USPT	fpga and application specific and integrated circuit	476	<u>L1</u>



# Search Results -

Term	Documents
MULTIPLEXOR.USPT.	4755
MULTIPLEXORS.USPT.	1645
(6 AND MULTIPLEXOR).USPT.	13

Database:	US Patents Full-Text Database	▼	
Refine S	l6 and multiplexor  Search:		
	Search Histo	orv	***************************************

DB Name	<u>Query</u>	Hit Count	Set Name
USPT	16 and multiplexor	13	<u>L7</u>
USPT	fpga and asic	583	<u>L6</u>
USPT	fpga and aic	9	<u>L5</u>
USPT	fpga and asla	1	<u>L4</u>
USPT	12 and interconnect and routing	141	<u>L3</u>
USPT	11 and multiplex\$	284	<u>L2</u>
USPT	fpga and application specific and integrated circuit	476	<u>L1</u>